



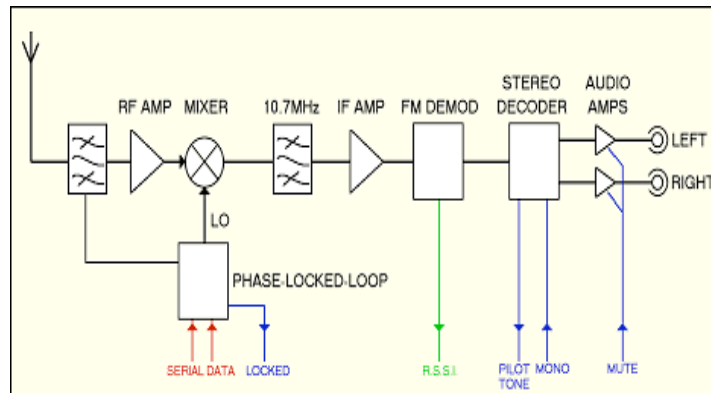
PIC Programming in C and Assembly

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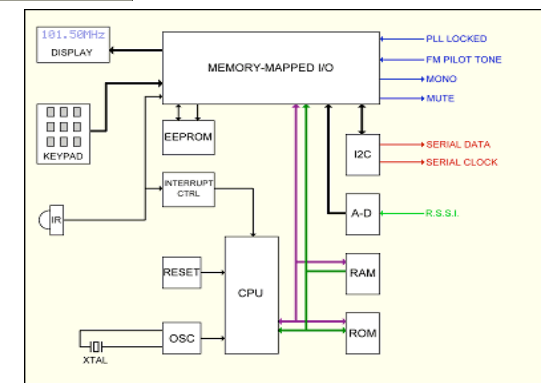
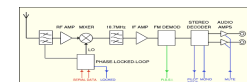
Outlines

- Microprocessor vs. MicroController
- PIC in depth
- PIC Programming
 - Assembly Programming
 - Embedded C
 - In-line assembly
- PIC Interface

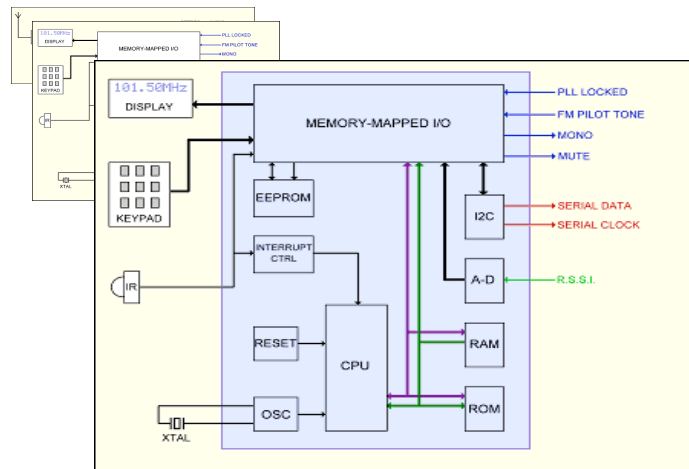
Microcontroller vs. Microprocessor



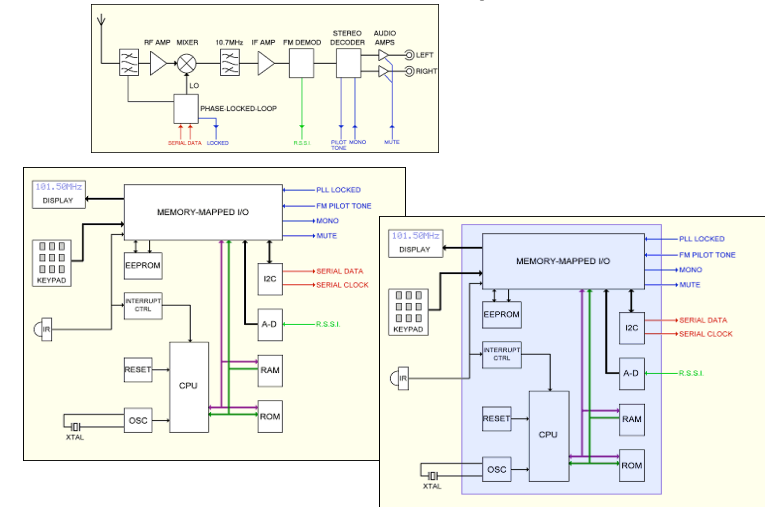
Microcontroller vs. Microprocessor



Microcontroller vs. Microprocessor



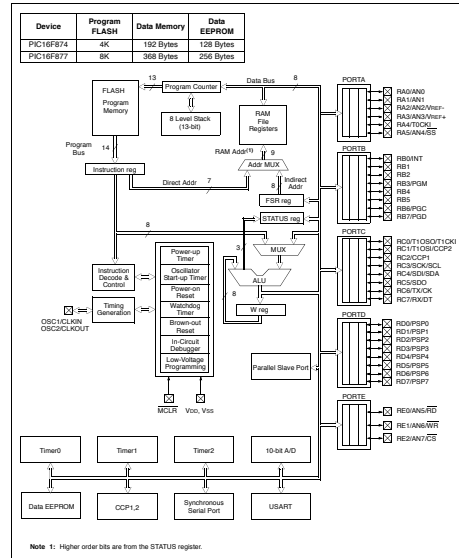
Microcontroller vs. Microprocessor



Figures from <http://www.mhennessey.f9.co.uk/pic/introduction.htm>

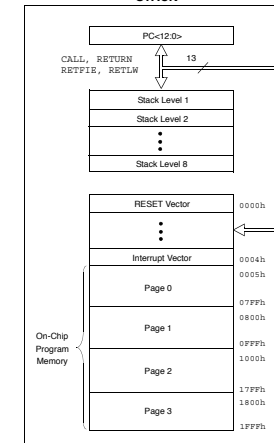
PIC in depth

FIGURE 1-2: PIC16F874 AND PIC16F877 BLOCK DIAGRAM



Memory Model

FIGURE 2-1: PIC16F877/876 PROGRAM MEMORY MAP AND STACK



Register File Map

FIGURE 2-3: PIC16F877/876 REGISTER FILE MAP

File Address	File Address	File Address	File Address
Indirect addr ¹⁾ 00h	Indirect addr ¹⁾ 00h	Indirect addr ¹⁾ 100h	Indirect addr ¹⁾ 100h
TMR0 01h	OPTION_REG 81h	TMR0 101h	OPTION_REG 181h
PCL 02h	PCL 82h	PCL 102h	PCL 182h
STATUS 03h	STATUS 83h	STATUS 103h	STATUS 183h
FSR 04h	FSR 84h	FSR 104h	FSR 184h
PORTA 05h	TRISA 85h	PORTA 105h	TRISA 185h
PORTB 06h	TRISB 86h	PORTB 106h	TRISB 186h
PORTC 07h	TRISC 87h	PORTC 107h	TRISC 187h
PORTD ²⁾ 08h	TRISD ²⁾ 88h	PORTD ²⁾ 108h	TRISD ²⁾ 188h
PORTE ²⁾ 09h	TRISE ²⁾ 89h	PORTE ²⁾ 109h	TRISE ²⁾ 189h
INTCON 0Ah	PCLATH 8Ah	PCLATH 10Ah	PCLATH 18Ah
INTCON 0Bh	INTCON 8Bh	INTCON 10Bh	INTCON 18Bh
PIR1 0Ch	PIE1 8Ch	EEDATA 10Ch	ECON1 18Ch
PIR2 0Dh	PIE2 8Dh	EEDATA 10Dh	ECON2 18Dh
TMR1L 0Eh	PCON 8Eh	EEDATA 10Eh	Reserved ²⁾ 18Eh
TMR1H 0Fh	EEADR ²⁾ 8Fh	EEDATA 10Fh	Reserved ²⁾ 18Fh
T1CON 10h	SSPCON2 90h	General Purpose Register 10h	General Purpose Register 10h
TMR2 11h	SSPCON2 91h	General Purpose Register 11h	General Purpose Register 11h
T2CON 12h	PR2 92h	General Purpose Register 12h	General Purpose Register 12h
SSPBUF 13h	SSPAD0 93h	General Purpose Register 13h	General Purpose Register 13h
SSPCON 14h	SSPSTAT 94h	General Purpose Register 14h	General Purpose Register 14h
CCPR1L 15h	CCPR1H 95h	General Purpose Register 15h	General Purpose Register 15h
CCP1CON 16h	CCP1CON 96h	General Purpose Register 16h	General Purpose Register 16h
CCP1CON 17h	CCP1CON 97h	General Purpose Register 17h	General Purpose Register 17h
TACSTA 18h	TACSTA 98h	General Purpose Register 18h	General Purpose Register 18h
TXREG 19h	SPBRG 99h	General Purpose Register 19h	General Purpose Register 19h
RCREG 1Ah	RCREG 9Ah	General Purpose Register 1Ah	General Purpose Register 1Ah
CCPR2L 1Bh	CCPR2H 9Bh	General Purpose Register 1Bh	General Purpose Register 1Bh
CCP2CON 1Ch	CCP2CON 9Ch	General Purpose Register 1Ch	General Purpose Register 1Ch
CCP2CON 1Dh	CCP2CON 9Dh	General Purpose Register 1Dh	General Purpose Register 1Dh
ADRESH 1Eh	ADRESH 9Eh	General Purpose Register 1Eh	General Purpose Register 1Eh
ADCON0 1Fh	ADCON0 9Fh	General Purpose Register 1Fh	General Purpose Register 1Fh
ADCON0 20h	ADCON0 100h	General Purpose Register 20h	General Purpose Register 20h
General Purpose Register 96 Bytes	General Purpose Register 80 Bytes	General Purpose Register 16 Bytes	General Purpose Register 16 Bytes
Bank 0 7Fh	Bank 1 EFh	Bank 2 16Fh	Bank 3 1EFh
	accesses 70h-7Fh	accesses 70h-7Fh	accesses 70h-7Fh
	FFh	17Fh	1FFh

□ Unimplemented data memory locations, read as '0'.
Not a physical register.

Note 1: These registers are not implemented on the PIC16F876.
Note 2: These registers are reserved, maintain these registers clear.

Device?

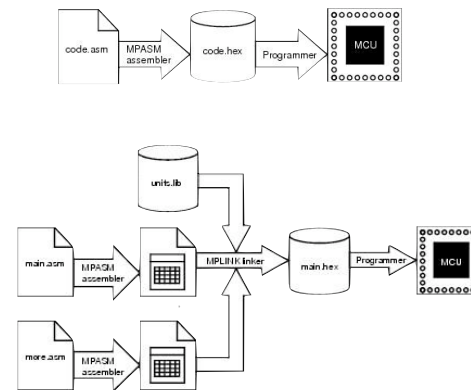
- I/O Port
 - Watchdog Timer
 - EEPROM
 - Timer & PWM
 - Serial Port
 - A2D
 - Interrupt
- Can you use any of them?

Assembly 101

- Assembly is the language closest to hardware level (machine language level).
- Why assembly?
 - Low level
 - speed
 - time critical
 - small program size

When to program in assembly?

Development Cycle



Component of Assembly

- Labels
 - start in column 1. may follow by a colon (:), space, tab or the end of line.
 - begin with an alpha character or an under bar (_) and may contain alphanumeric characters, the under bar and the question mark.
- Mnemonics, Directives and Macros
- Operands
- Comments

Example

Notice the directive

```

list      p=18f452
#include  p18f452.inc
Dest equ  0x0B      ;Define constant
org      0x0000      ;Reset vector
goto     Start
org      0x0020      ;Begin program

Start
    movlw 0x0A
    movwf Dest
    bcf   Dest, 3 ;This line uses 2
operands
    goto  Start
end
  
```

List File

Notice the directive

```

--- C:\PIC\SAMPLE.ASM
0000 2820 GOTO 0x20
0020 300A MOVLW 0xa
0021 008B MOVWF 0xb
0022 118B BCF 0xb, 0x3
0023 2820 GOTO 0x20

1:      list      p=16f877 , b=2, c=80
2:      #include  p16f877.inc
3:      Dest equ  0x0B      ;Define constant
4:      org      0x0000      ;Reset vector
5:      goto     Start
6:      org      0x0020      ;Begin program
7:      Start
8:      movlw 0x0A
9:      movwf Dest
10:     bcf   Dest, 3 ;This line uses 2 oper
11:     goto  Start
  
```

Quick tip

Use “banksel” directive to avoid bank switching issue.

```

BSF STATUS,RS0
movlw 0x00
movwf TRISB

0020 1683 BSF 0x3, 0x5 8: banksel TRISB
0022 3000 MOVLW 0 9: movlw 0x00
0023 0086 MOVWF 0x6 10: movwf TRISB
  
```

Macro

```
BCF 0x3, 0x5
MOVLW 0x55
MOVWF PORTB

OutPort macro myport, myvalue
    banksel myport
    movlw myvalue
    movwf myport
endm

BCF 0x3, 0x5
MOVLW 0xaa
MOVWF PORTA
```



```
OutPort PORTB, 0x55
OutPort PORTA, 0xAA
```

Advanced Macro

```
Add1_x macro x
    local i=0
    clrw
    while i<x
        addlw i
        i += 1
    endw
endm

Add1_x 10
; This will give
CLRW
ADDLW 0
ADDLW 0x1
ADDLW 0x2
....
....
ADDLW 0xe
ADDLW 0xf
```

See MPASM HELP
for more details.

Embedded C

```
Lab1 in C

#include <pic16f87.h>
void main (void) {
    TRISB=0x01;
    while ( 1 ) {
        if (RB0) {
            PORTB |=0x02;
            PORTB &=0xFB;
            //PORTB=0x02;
        } else {
            PORTB |=0x04;
            PORTB &=0xFD;
            //PORTB=0x04;
        }
    }
}
```

Mix C and Assembly

```
#include <pic16f87.h>
```

```
void main (void) {
```

```
    TRISB=0x01;
```

```
    while ( 1 ) {
```

```
        if (RB0) {
```

```
            #asm
```

```
            BSF _PORTB, 1
```

```
            BCF _PORTB, 2
```

```
            #endasm
```

```
        } else {
```

```
            asm("BCF _PORTB, 1");
```

```
            asm("BSF _PORTB, 2");
```

```
        }
```

```
    }
```

```
}
```

● Inline assembly

● Use #asm and #endasm for multiple line.

● Use asm(" ") for single line

● Add _ to variable in C

Good library can
help inexperienced
programmer.

See 16 adders in C

```
int sum(int i, int j) {
```

```
    int sum;
```

```
    sum=i+j;
```

```
    return sum;
```

```
}
```

```
CLRF 0x3
```

```
MOVF 0x22, W
```

```
ADDWF 0x20, W
```

```
MOVWF 0x20
```

```
MOVF 0x23, W
```

```
BTFSC 0x3, 0
```

```
INCF 0x23, W
```

```
ADDWF 0x21, W
```

```
MOVWF 0x21
```

```
MOVWF 0x7f
```

```
MOVF 0x20, W
```

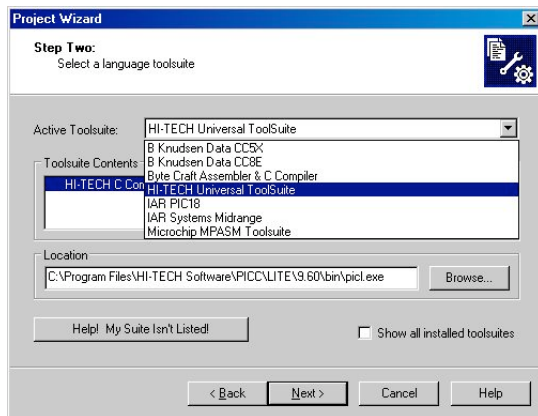
```
MOVWF 0x7e
```

```
RETURN
```

How is your LAB 2?

What if there is no library to do your job?

How to use C in my LAB?



When to write in C?
When to write in Assembly?

POP Quiz:

Will recursive program work in PIC?