Research: Why and How

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My research works

Rearch area:

Evolutionary computation Genetic Algorithm Genetic Programming Evolution Strategy

Machine Learning
Artificial Neuron Network
Decision Tree
Inductive Logic Program
Support Vector Machine

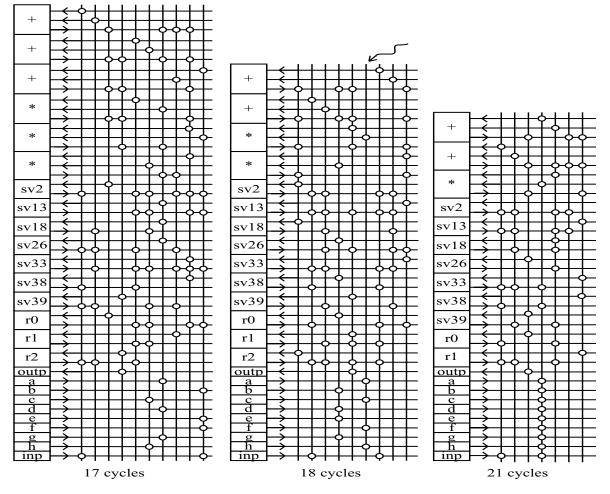
Embedded Systems Virtual Machines Special-purpose Programming Languages Embedded Processors Reconfigurable Circuits

Design by Evolution
Robot Programs
Digital Circuits
Finite State Machines

Algorithms Building Blocks Compressed Representation Multiple Objectives Optimisation Genetic Algorithm Circuits Parallelisation

Robotics

Applications
Bioinformatics
Forecasting
3-d bin packing



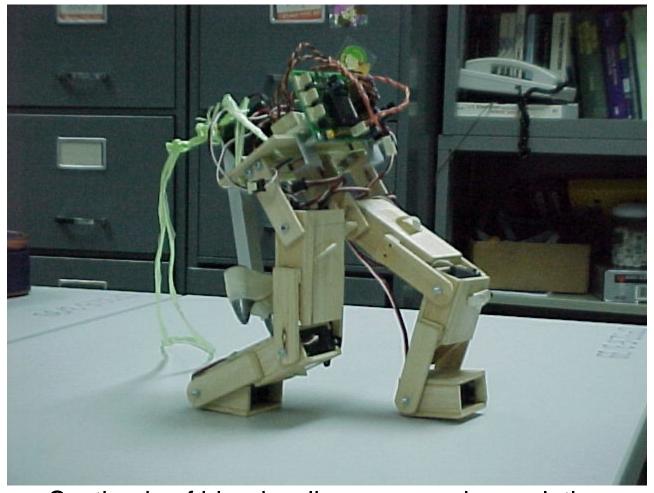
High level synthesis by Ant algorithm

C. Aporntewan and P. Chongstitvatana

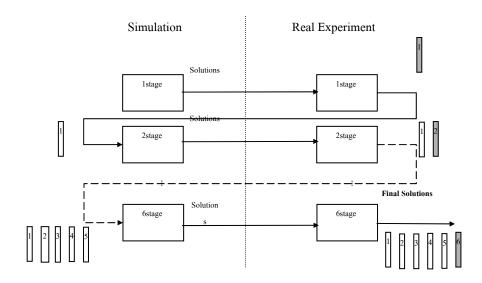
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M = (m_{ij}) denotes \ell \times \ell chi-square matrix.
T_i and R_{i,j} denote arrays of numbers.
A and B are partition subsets.
P denotes a partition.
Algorithm PAR(M, \alpha)
P \leftarrow \emptyset;
for i = 1 to \ell do // outer loop
     if i \notin B for all B \in P then
          T \leftarrow \{\text{row } i \text{ sorted in desc. order}\};
          for j = 1 to \ell do
              R_{i,j} \leftarrow x \text{ where } m_{ix} = T_j;
          endfor
          A \leftarrow \{i\};
          B \leftarrow \{i\};
          for j = 1 to \ell - 2 do // inner loop
              A \leftarrow A \cup \{R_{i,j}\};
              if A satisfies cond. 3.1 and 4.1 then
                   B \leftarrow A;
          endfor
          P \leftarrow P \cup \{B\};
     endif
endfor
return P;
```

Fig. 9. Algorithm PAR takes an $\ell \times \ell$ symmetric matrix, $M = (m_{ij})$. The outpu partition of $\{1, \ldots, \ell\}$

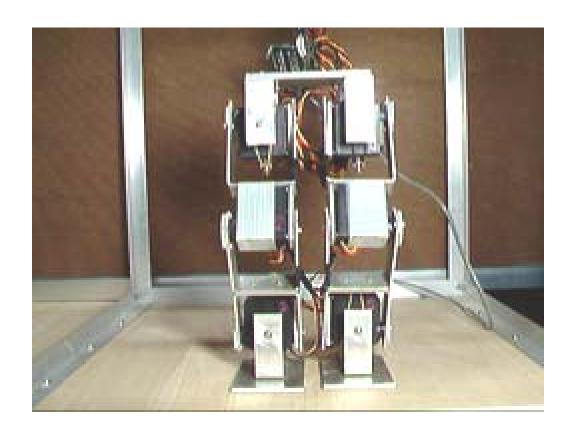


Synthesis of biped waling program by evolution



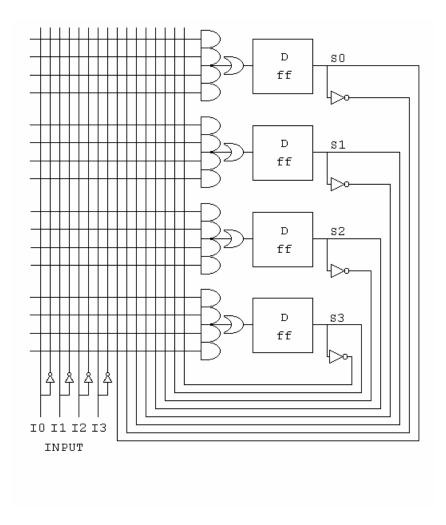
Staged evolution with real-world embedding

second generation 10 DOF biped

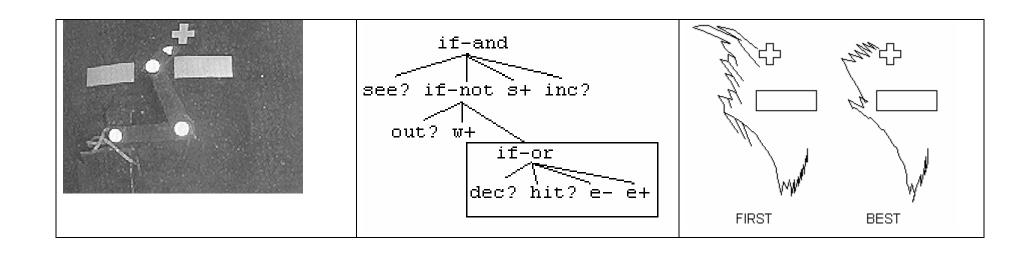




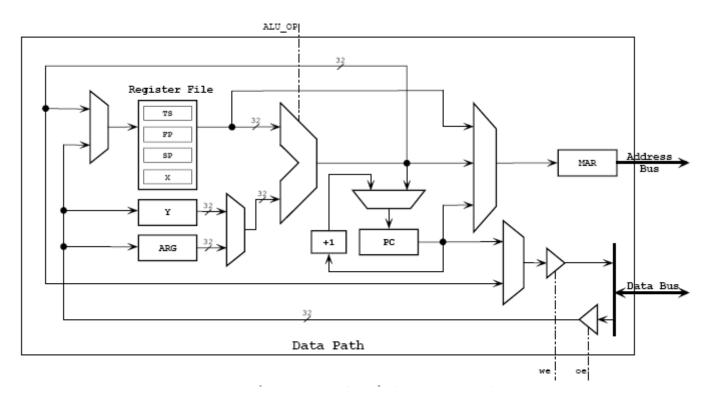
synthesis of life-like plants



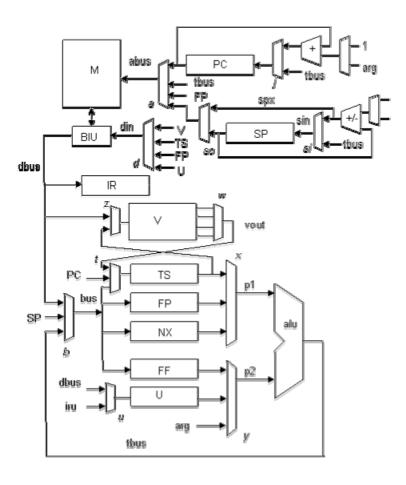
Synthesis of sequential circuits



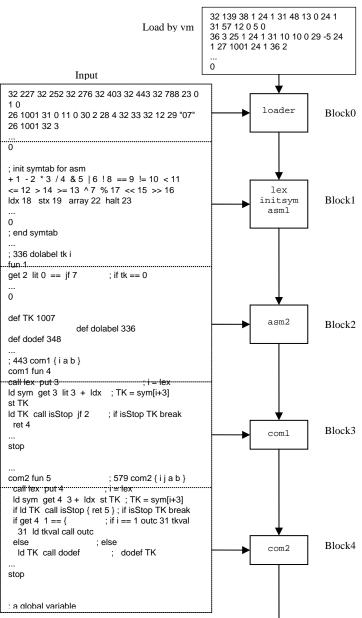
Synthesis of robot arm control program



Stack-based resource efficient processor



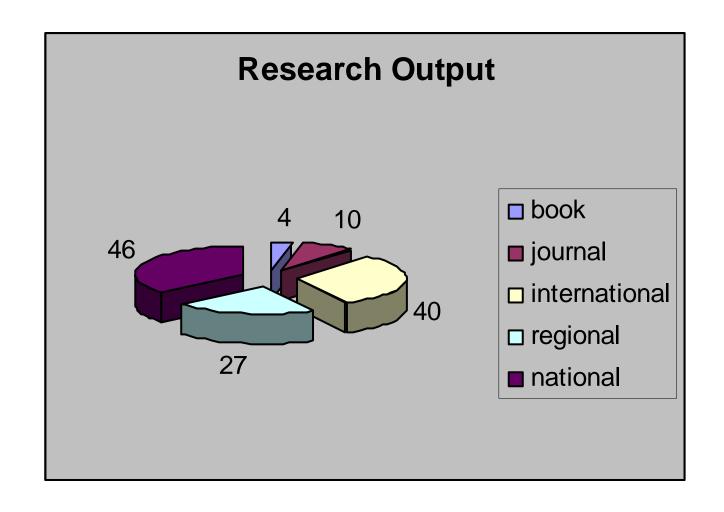
Stack-caching for performance improvement



at Department of Computer Engineering, Kasetsart University, 15 January 2008

32 25 23 0 38 1 24 1 36 1 40 2 38 3 31 1 25 2 31 0 25 1 24 2 24 3 12 0 30 8 24 1 24 4 24 2 1 0 25 1 34 2 machine code 28 -10 24 1 40 6 38 1 31 20 22 0

Self-generating Compiler



Research Students Graduated

Master 30 Doctor 5

current: Master 3, Doctor 4

Research as an Evolution

Ph.D
work with students
funding agency
applications
multi-discipline



